**Project 2 Evaluation**

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| --- | --- | --- | --- |
| Name: | | Total: | |
| Checklist | Weight | Earned | Notes |
| 1. Description of your design approach for both matrix – matrix multiplication and matrix – vector multiplication modes | 15 |  |  |
| 1. Tradeoff on input and output architecture | 5 |  |  |
| 1. Screen shots of model composer files | 15 |  |  |
| 1. Screen shots of simulation results | 15 |  |  |
| 1. Screen shots of hardware co-simulation results | 10 |  |  |
| 1. Resource utilization | 10 |  |  |
| 1. Timing information from the testing phase | 5 |  |  |
| 1. Description of scalability of design and the ability to pipeline a series of matrix multiplications on your array and the latency and delay characteristics. | 10 |  |  |
| 1. Description of testing methodology | 5 |  |  |
| 1. Turning in Matlab files .slx and .m | 10 |  |  |